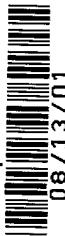


**UTILITY PATENT APPLICATION
UNDER 37 CFR 1.53(b)**

JC997 U.S. PRO
09/927368



Box PATENT APPLICATION
Commissioner for Patents
Washington, DC 20231
Sir:

Transmitted herewith for filing is the patent application of:

INVENTOR: Hisaya MORI, Shinji YAMADA, Teruhiko FUNAKURA
FOR: APPARATUS AND METHOD FOR TESTING SEMICONDUCTOR
INTEGRATED CIRCUIT

Enclosed are:

- 38 pages of specification, claims, abstract.
 Declaration and Power of Attorney.
 Priority Claimed.
 Certified copy of Japanese Patent Application No. 2001-032596
 11 sheets of formal drawing.
 An assignment of the invention to MITSUBISHI DENKI KABUSHIKI KAISHA,
and RYODEN SEMICONDUCTOR SYSTEM
ENGINEERING CORPORATION

and the assignment recordation fee.

An associate power of attorney.

A verified statement to establish small entity status under 37 CFR 1.9 and 37 CFR 1.27.
 Information Disclosure Statement, Form PTO-1449 and reference.

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The filing fee has been calculated as shown below:

	NO. OF CLAIMS	EXTRA CLAIMS	RATE	AMOUNT
Total Claims	6	-20	\$18.00	\$0.00
Independent Claims	2	-3	\$80.00	\$0.00
Multiple Dependent Claim(s)				\$0.00
Basic Fee				\$710.00
Total of Above Calculations				\$710.00
Less ½ for Small Entity				\$0.00
Assignment & Recording Fee				\$40.00
Total Fee				\$750.00



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PATENT TRADEMARK OFFICE

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 - Any patent application processing fees under 37 CFR 1.17.
 - Any filing fees under 37 CFR 1.16 for presentation of extra claims.

Respectfully submitted,

MCDERMOTT, WILL & EMERY


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